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## The RESAuto Project

Investigating Capability  
Enhanced Microprocessors  
for Cyber Resilient  
Automotive Systems

11<sup>th</sup> July 2024





## Search “WMG Warwick”

- WMG is an applied research and education faculty of the University of Warwick
- Our focus is on industry and business impact through R&D and skills provision
- We have relationships with over 1000 companies
- We provide degree education at Apprentice, UG, PG and research levels
- We run short courses and work-based learning
- Our engineering disciplines, including **resilient and smart manufacturing; sustainable materials; energy transition; transportation; data, connectivity and immersive tools.**



QR code for WMG Home Page

<https://warwick.ac.uk/fac/sci/wmg/>

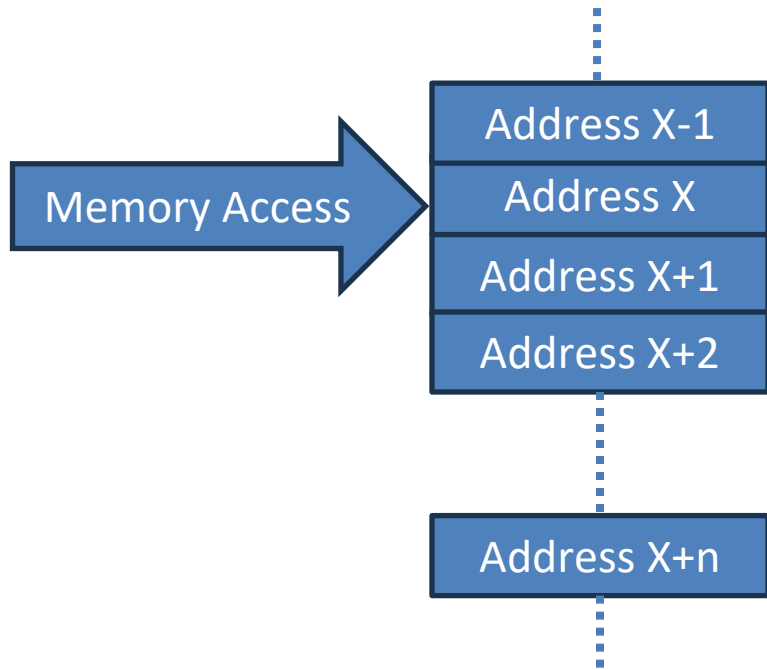
- Cyber resilience for Cyber-Physical Systems (CPS).
- A CPS cannot rely on traditional enterprise-style cybersecurity of passive monitoring and reactive defence.
- A CPS must operate safely under cyber attack and demonstrate cyber resilience
- A CPS must resist attacks and degrade safely if functionality is compromised.
- For RESAuto – can **Capability Hardware Enhanced RISC Instructions (CHERI)** microcontrollers aid software resilience?



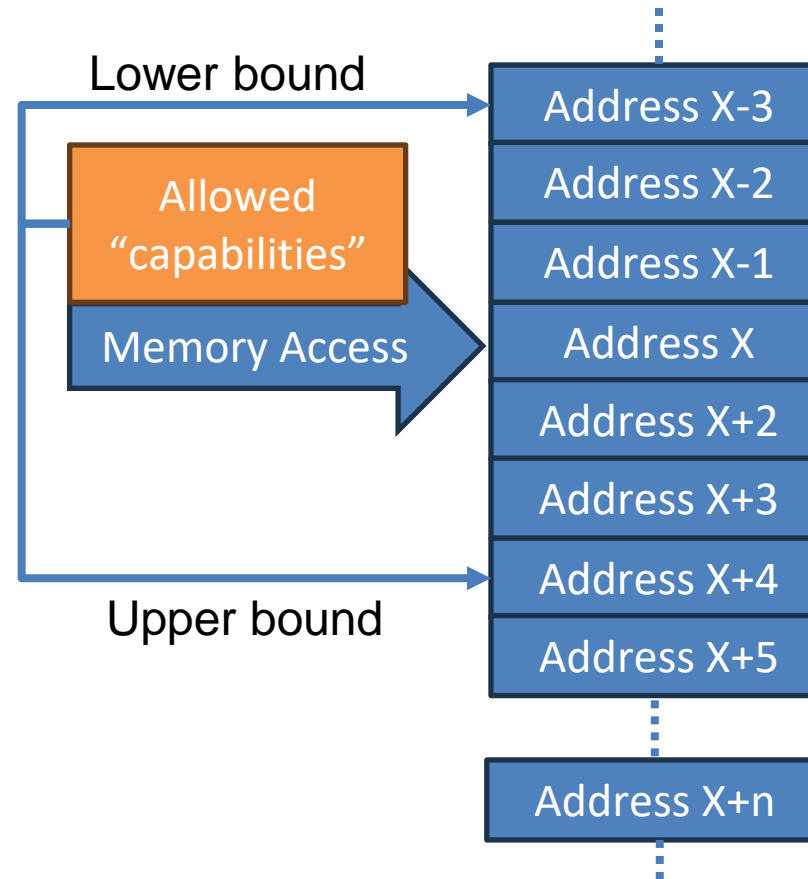
AI-generated image

# What is a capability-enhanced microprocessors?

Manipulating memory access is at the root of many security issues



Traditional Microprocessor

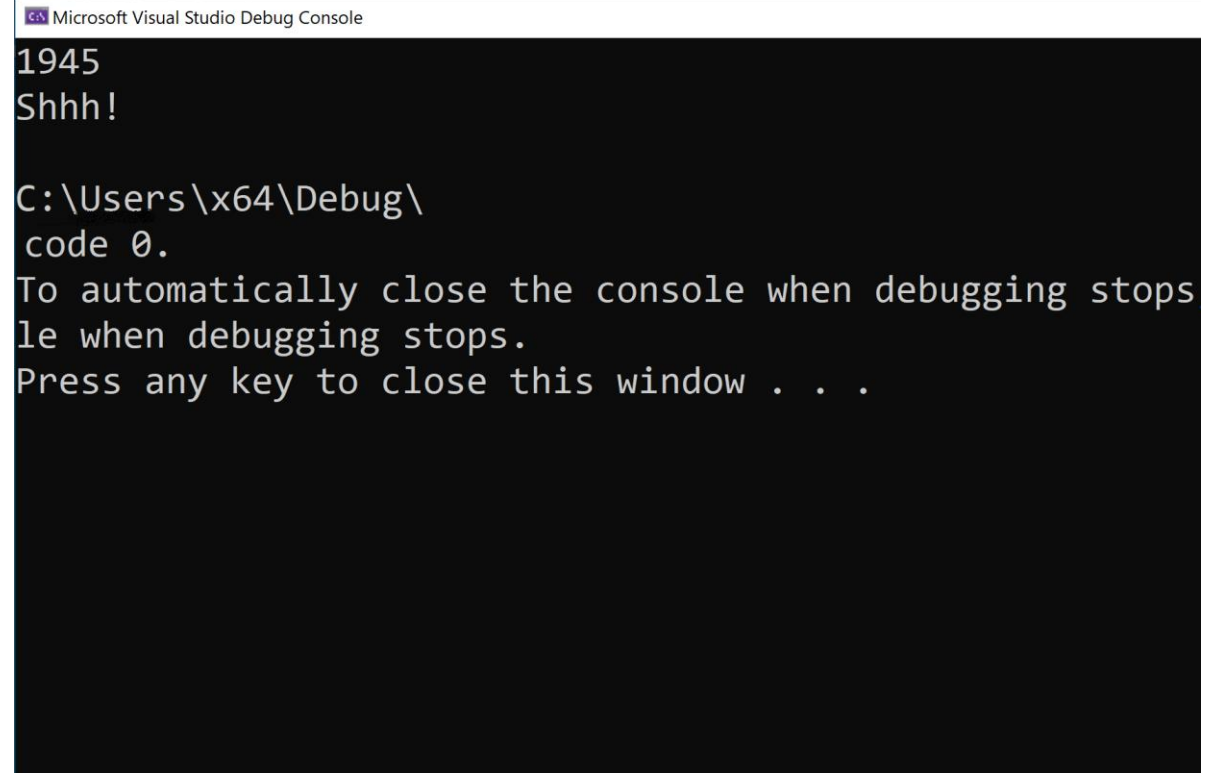


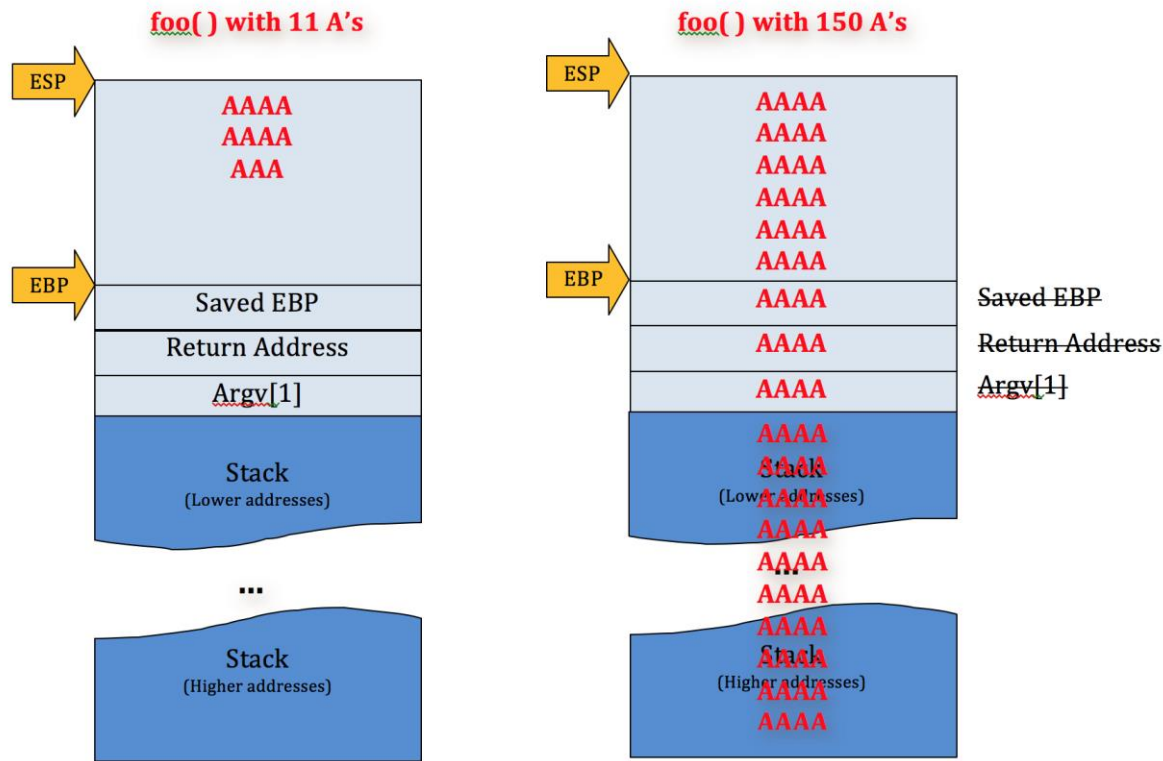
Capability Enhanced Microprocessor

Instruction tags add extra "capabilities" to memory "pointers"

# “with great power there must also come – great responsibility” (Stan Lee)

```
3
4   #include <stdio.h>
5
6   int x = 1;
7   int my_secretnumber = 1945;
8   char my_password[] = "Shhh!";
9
10  void funcA(int* ptr) {
11      ptr = ptr + 1;
12      int leaky_mem = *ptr;
13      printf("%d\n", leaky_mem);
14  }
15
16
17  void funcB(int *ptr) {
18      char* leaky_mem = ptr + 2;
19      printf("%s\n", leaky_mem);
20  }
21
22  int main()
23  {   int *pointer = &x;
24
25      funcA(pointer);
26      funcB(pointer);
27      return 0;
28  }
29
```





[6] CALL strcpy()

Copies value of Argv[1] into space reserved for local variable c

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## Stack-Based Buffer Overflows

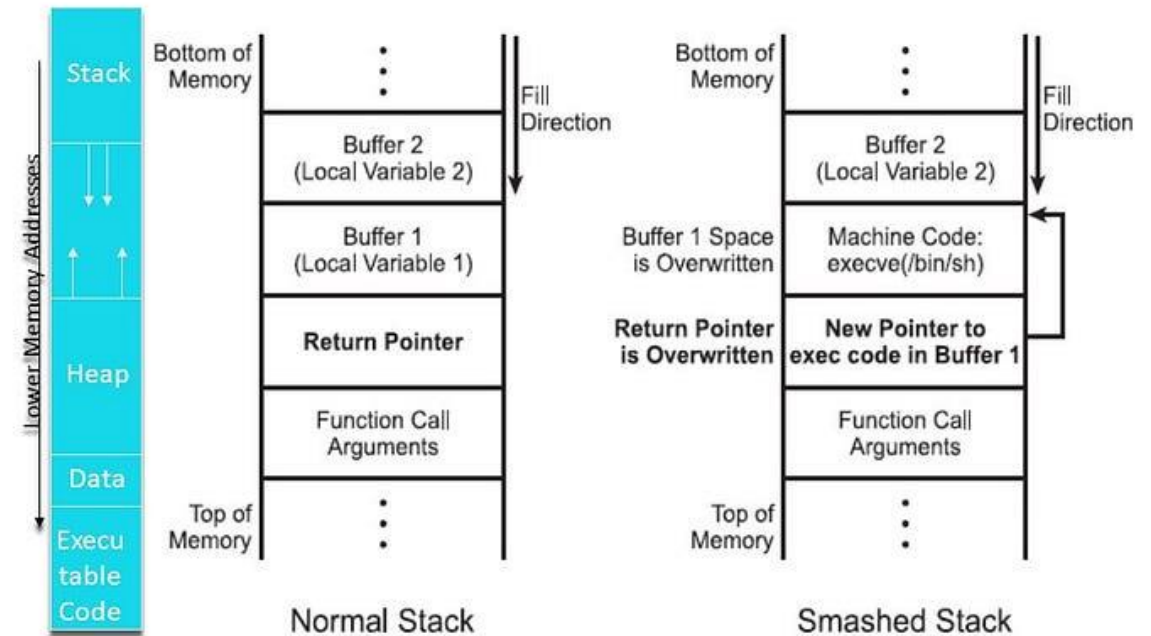


EXHIBIT 10.2 A normal stack and a stack with a buffer overflow.

# Common Vulnerabilities and Exposures (CVE)

## Vulnerabilities by type & year



<https://www.cvedetails.com/vulnerabilities-by-types.php>

# Acknowledging the memory bug issue

The Chromium Projects

Home  
Chromium  
ChromiumOS

Quick links  
Report bugs  
Discuss

Other sites  
Chromium Blog  
Google Chrome  
Extensions

Search [CTRL] [K]

Chromium > Chromium Security >

## Memory safety

The Chromium project finds that around 70% of our serious security bugs are [memory safety problems](#). Our next major project is to prevent such bugs at source.

### The problem

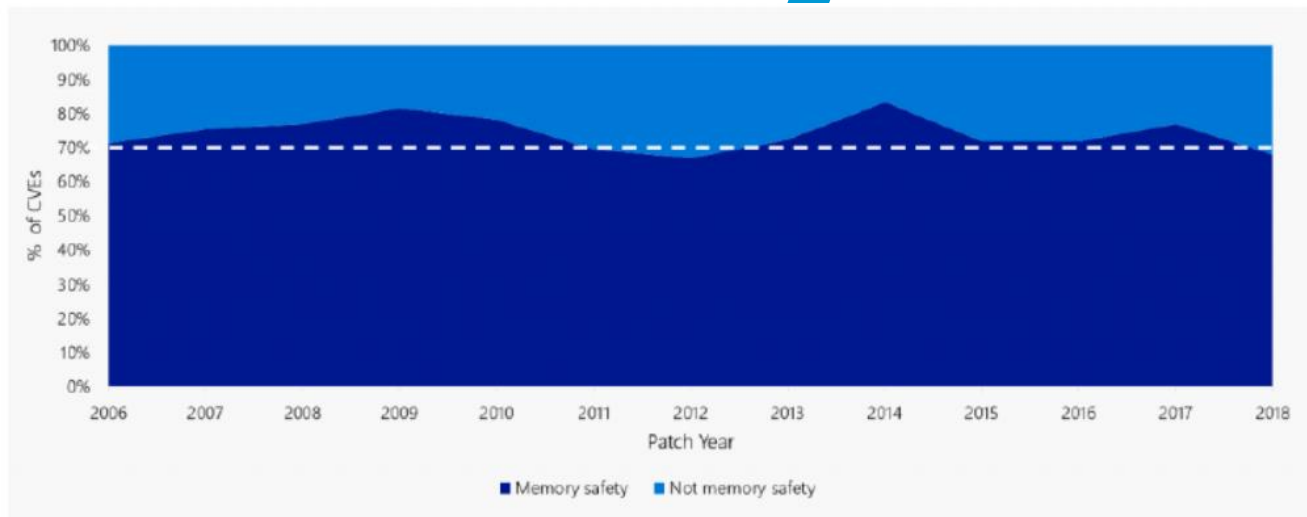
Around 70% of our high severity security bugs are memory unsafety problems (that is, mistakes with C/C++ pointers). Half of those are use-after-free bugs.

High+, impacting stable

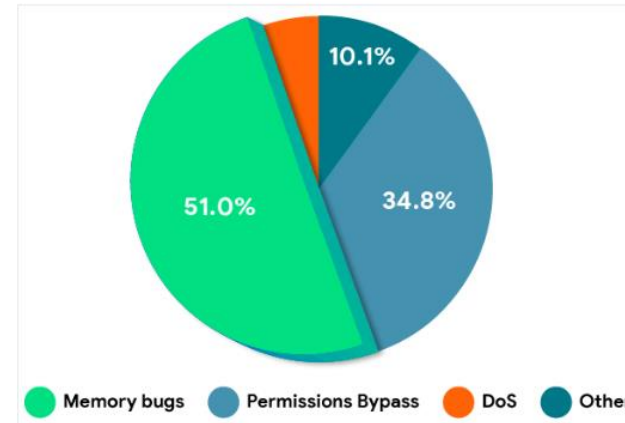
Category	Percentage
Use-after-free	36.1%
Other memory unsafety	32.9%
Other	23.9%
Security-related assert	7.1%

(Analysis based on 912 high or critical [severity](#) security bugs since 2015, affecting the Stable channel.)

These bugs are spread evenly across our codebase, and a high proportion of our non-security stability bugs share the same types of root cause. As well as risking our users' security, these bugs have real costs



~70% of the vulnerabilities Microsoft assigns a CVE each year continue to be memory safety issues



Memory safety bugs contribution to Android vulnerabilities

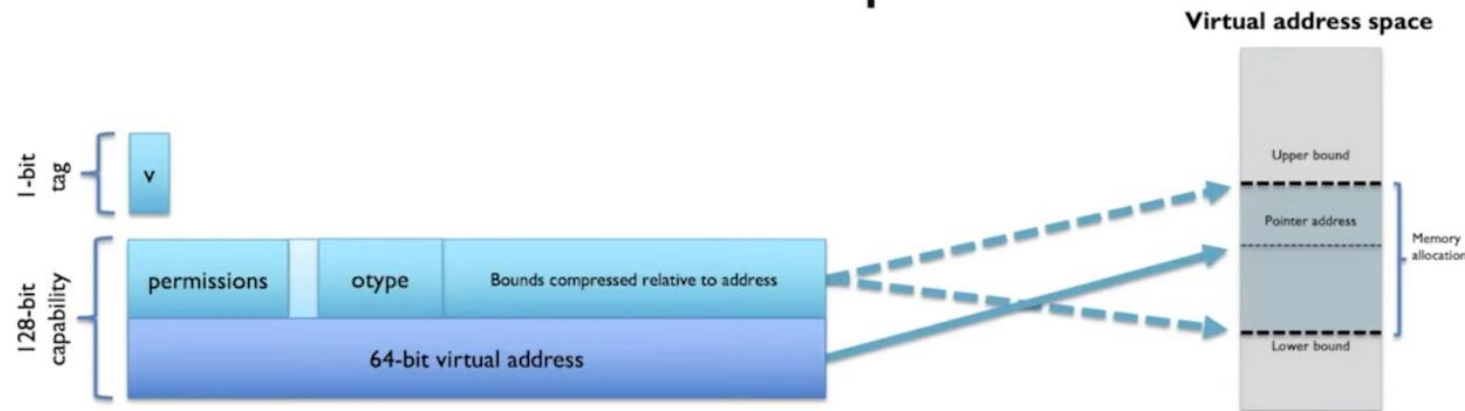


## A Few Memory Related CVEs Associated with Vehicles

- CVE-2023-39076 - Injecting data into the USB memory area causes a Denial of Service (DoS) in the in-car infotainment system - vehicles from GM Chevrolet
- CVE-2023-32157 - Heap-based Buffer Overflow Arbitrary Code Execution Vulnerability, lack of proper validation of the length of user-supplied data prior to copying it to a fixed-length heap-based buffer - vehicles from Tesla.
- CVE-2023-32155 - Write past the end of an allocated buffer results in Out-Of-Bounds Write Local Privilege Escalation - vehicles from Tesla
- CVE-2023-28885 - Denial of service via temporary failure of Media Player with a crafted MP3 file - vehicles from GM Chevrolet
- CVE-2021-23910 - There is an out-of-bounds array access in RemoteDiagnosisApp - vehicles from Mercedes-Benz
- CVE-2021-23906 - A Message Length is not checked in the HiQnet Protocol, leading to remote code execution - vehicles from Mercedes-Benz
- CVE-2020-27524 - Memory content leaks - vehicles from Audi
- CVE-2020-16142 - Bluetooth stack mishandles %x and %c format-string specifiers - vehicle from Mercedes-Benz
- CVE-2019-13582 - A stack overflow could lead to denial of service or arbitrary code execution - vehicles from Tesla
- CVE-2019-13581 - A heap-based buffer overflow allows remote attackers to cause a denial of service or execute arbitrary code via malformed Wi-Fi packets - vehicles from Tesla
- CVE-2017-9647 - A Stack-Based Buffer Overflow issue was discovered - vehicles from BMW, Ford, Infiniti, and Nissan
- CVE-2017-9633 - An Improper Restriction of Operations within the Bounds of a Memory Buffer issue was discovered - vehicles from BMW, Ford, Infiniti, and Nissan
- CVE-2012-2619 - Out-of-bounds read - vehicles from Ford

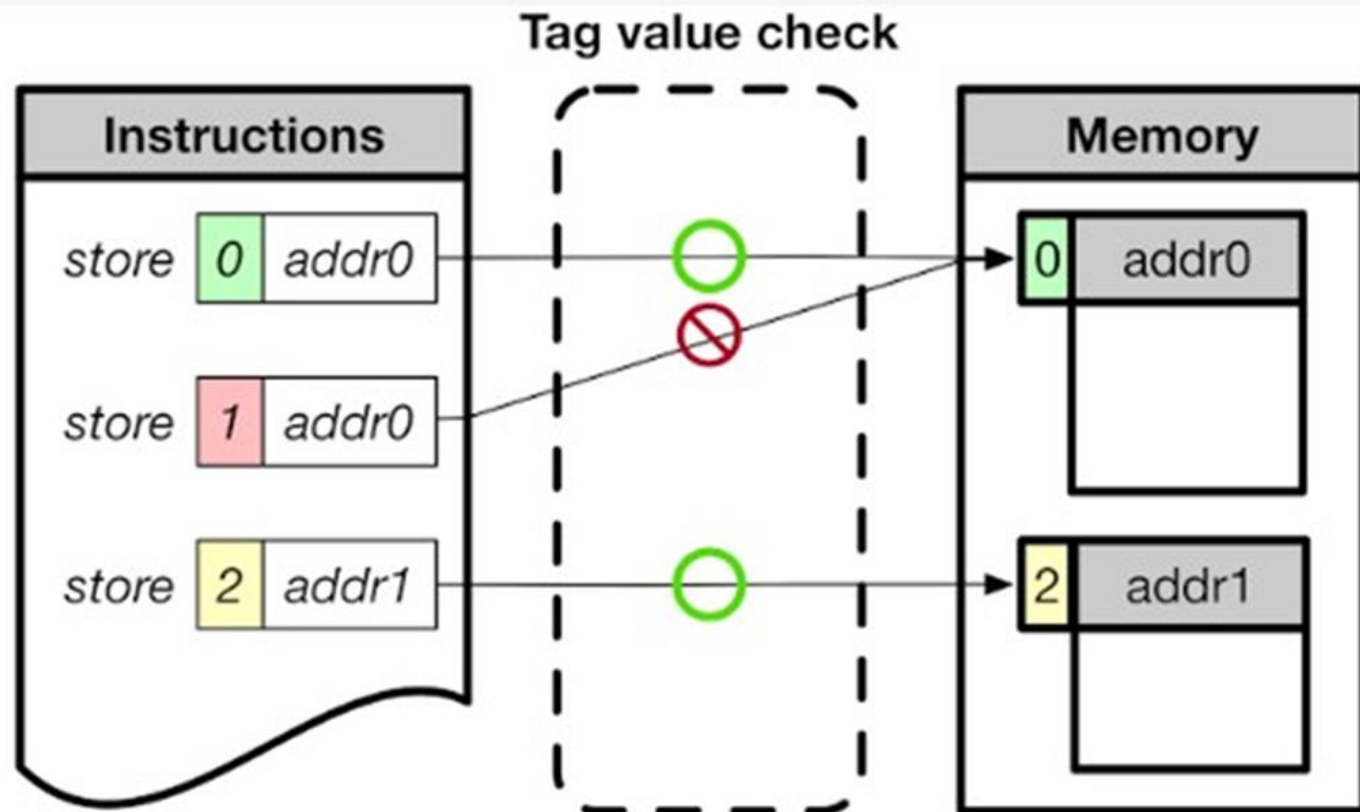
Ref: [https://cve.mitre.org/cve/search\\_cve\\_list.html](https://cve.mitre.org/cve/search_cve_list.html)

## CHERI 128-bit capabilities



CHERI pointers – 2x the size of traditional software pointers  
e.g., 128 bits on a 64-bit system + a validity tag bit

- **Capabilities** extend **integer memory addresses**
- **Metadata** (bounds, permissions, ...) control how it may be used
- **Tags** protect capability integrity/derivation in registers + memory



## A Capability Design



- E.g., the “CHERI” design from SRI International and the University of Cambridge.
- Implemented by the Arm “Morello” program.
- Morello is a prototype system-on-chip (SoC) and a development board that adopts the CHERI design.
- Are such “capability-enhanced” processors suitable for embedded device microcontrollers? E.g., vehicle electronic control units (ECUs).



<https://www.cl.cam.ac.uk/research/security/ctsr/cheri/>



Lead Partner



Demonstrator



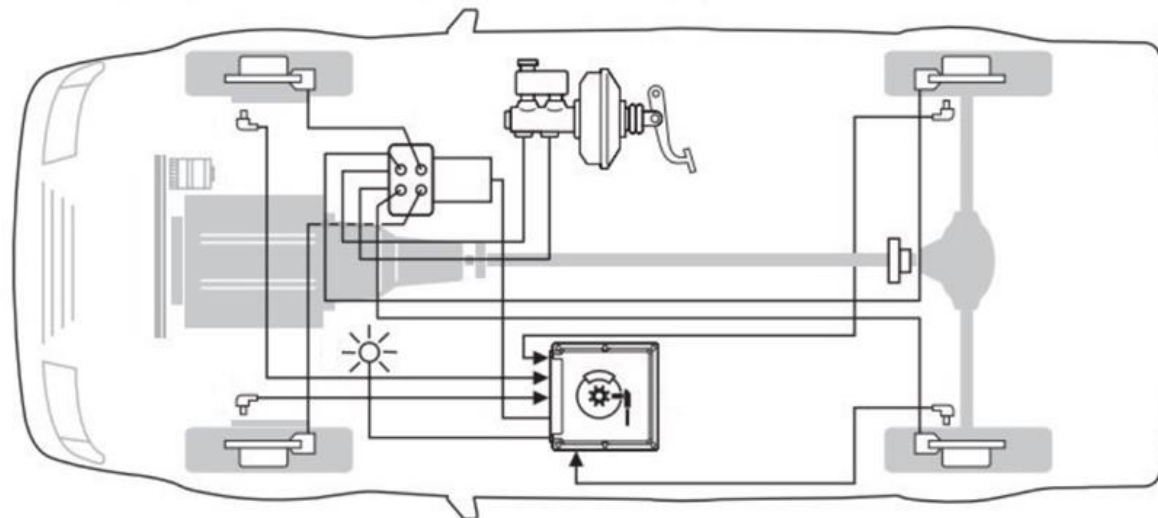
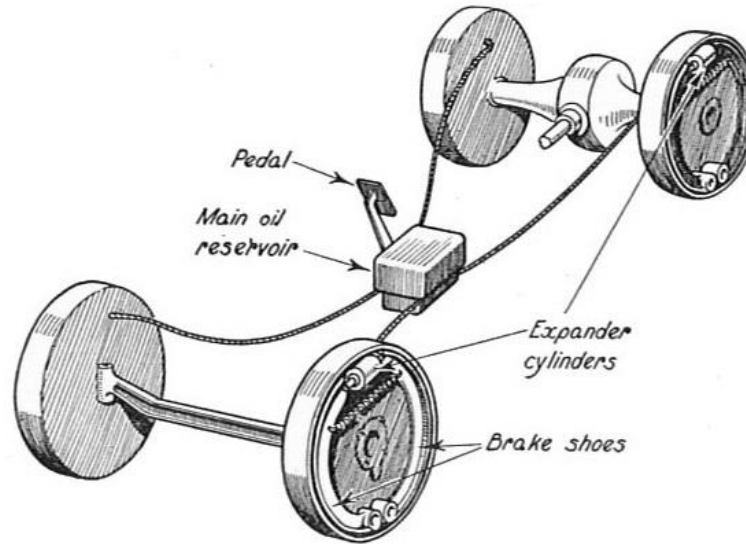
Dissemination

Funding:



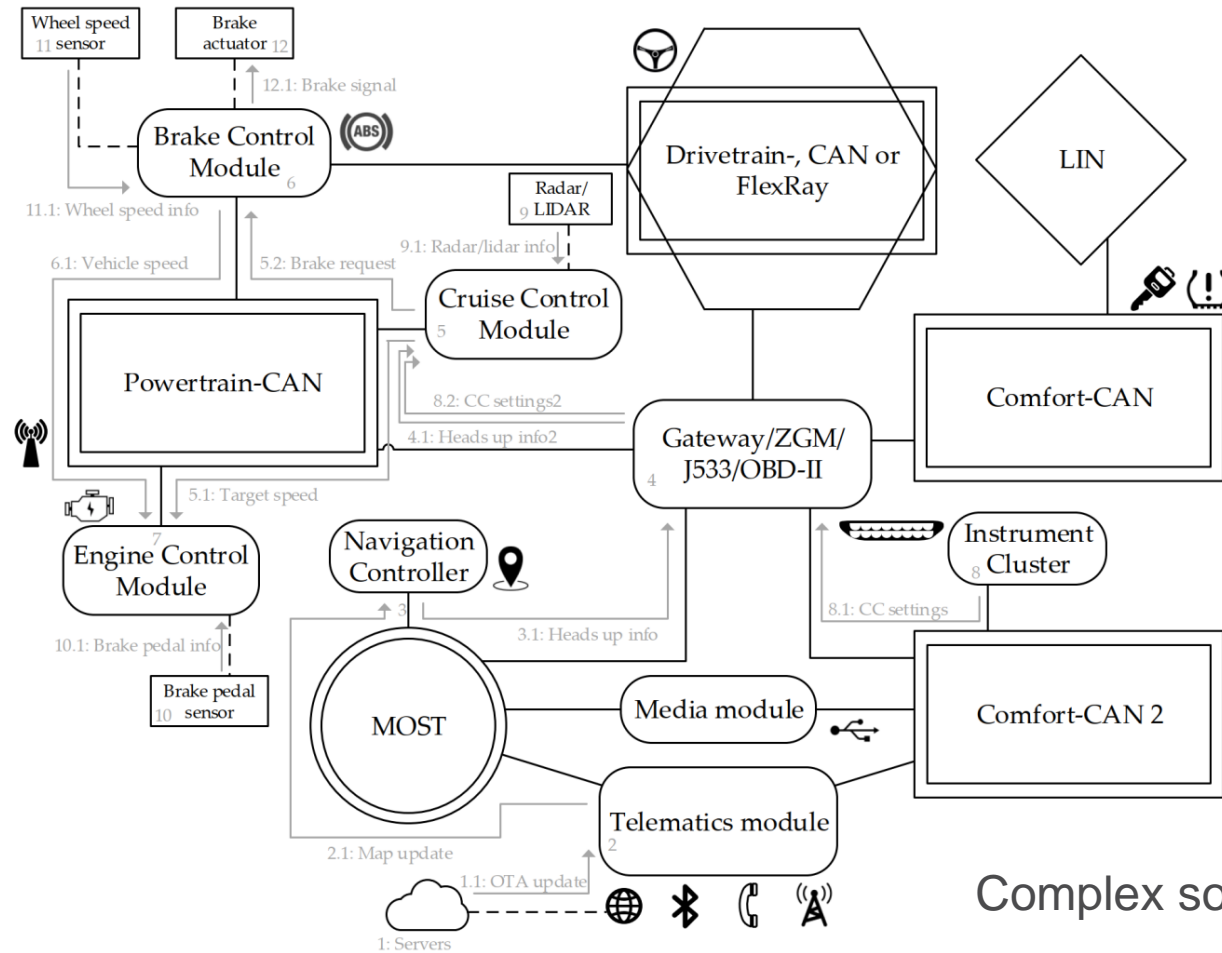
- New tooling – compilers and debuggers – Commodity compilers are available
- Requires additional programming knowledge – training considerations for future developers
- What is the impact on the supply chain?
  1. Intellectual Property (IP) Providers – compilers, software, libraries, tools, testing, equipment, secure coding guidelines
  2. Semiconductor/chip companies and tier suppliers – electronics, modules, and parts
  3. Original Equipment Manufacturers (OEMs) – the vehicle manufacturers
- The structural changes required to the engineering processes for these three groups
- What are the ethical, insurance and legal issues if organisations rely too much on the chip hardware for security?

# Increasing system complexity - automotive braking



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ecu\_brake\_system.jpg, Automotive Handbook, 11th Edition, Robert Bosch GmbH

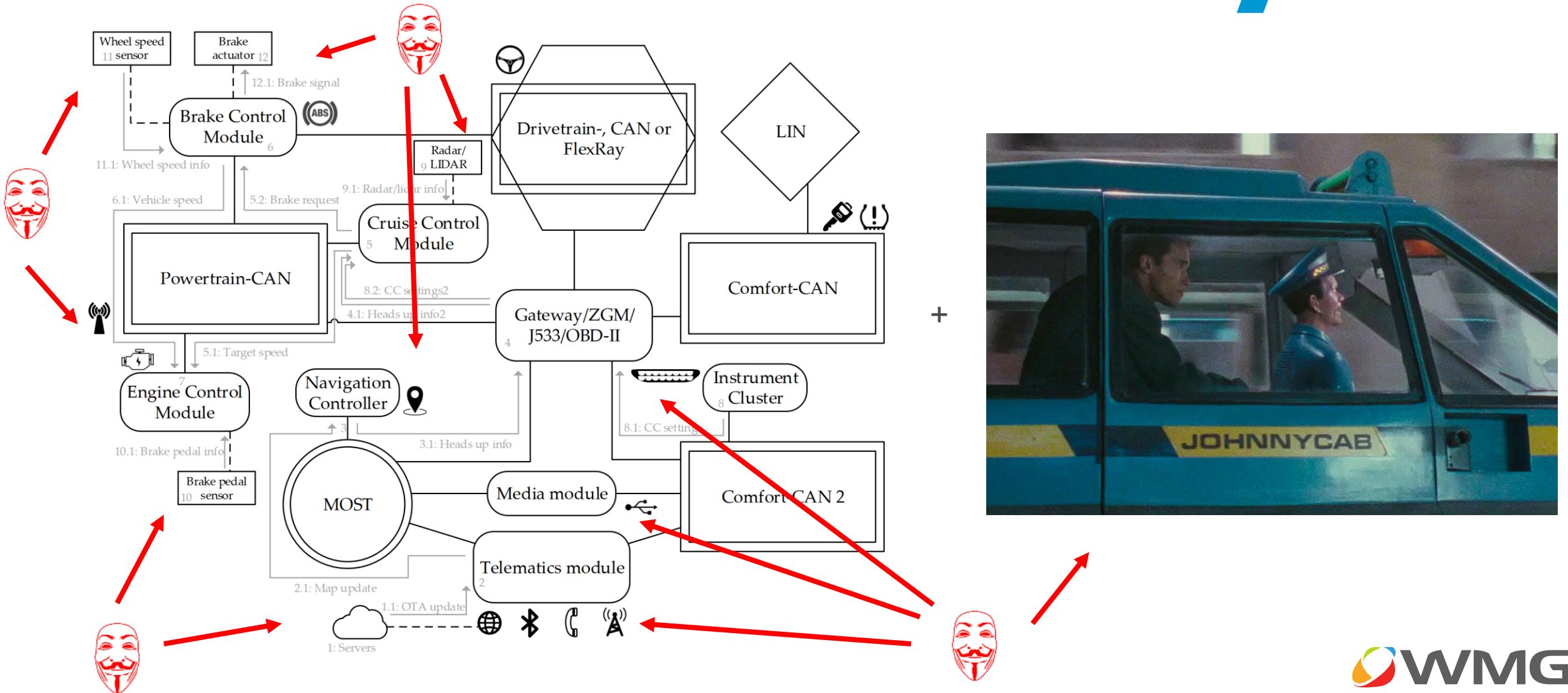
# The automotive braking system example



Complex software control + Artificial Intelligence

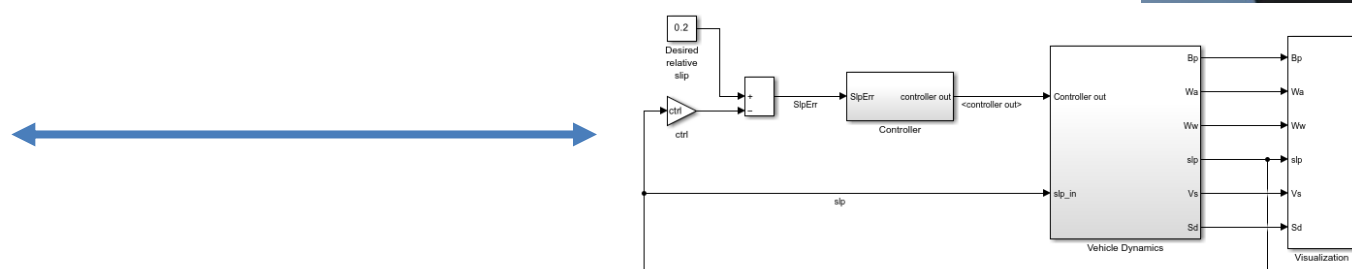
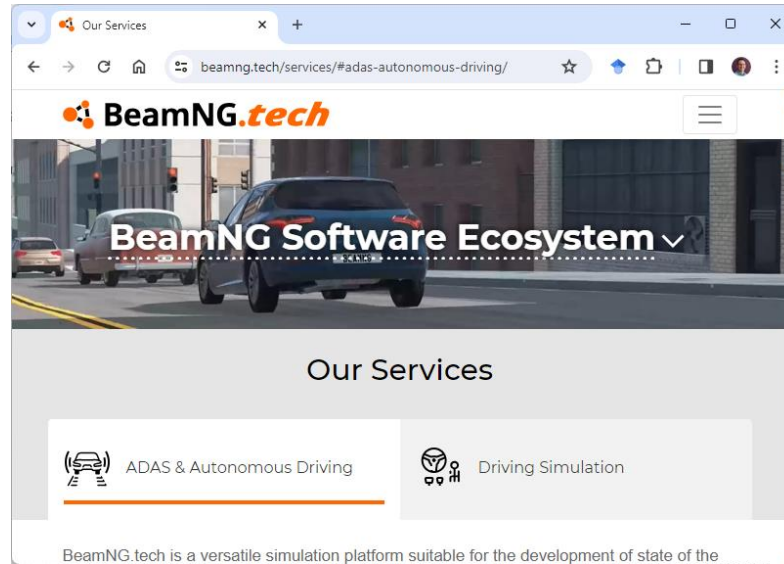


# Complexity increases the cyber attack surface



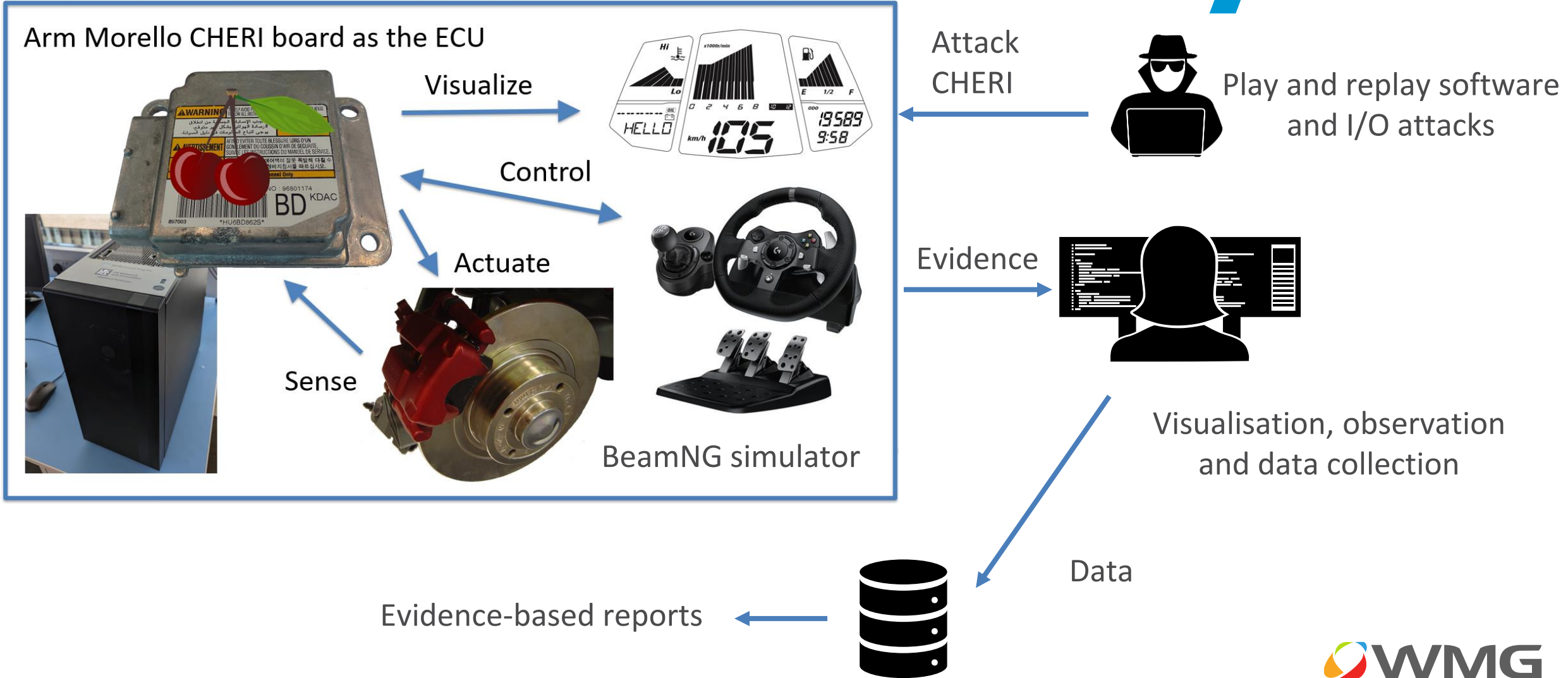
# WMG CHERI Braking Demonstrator

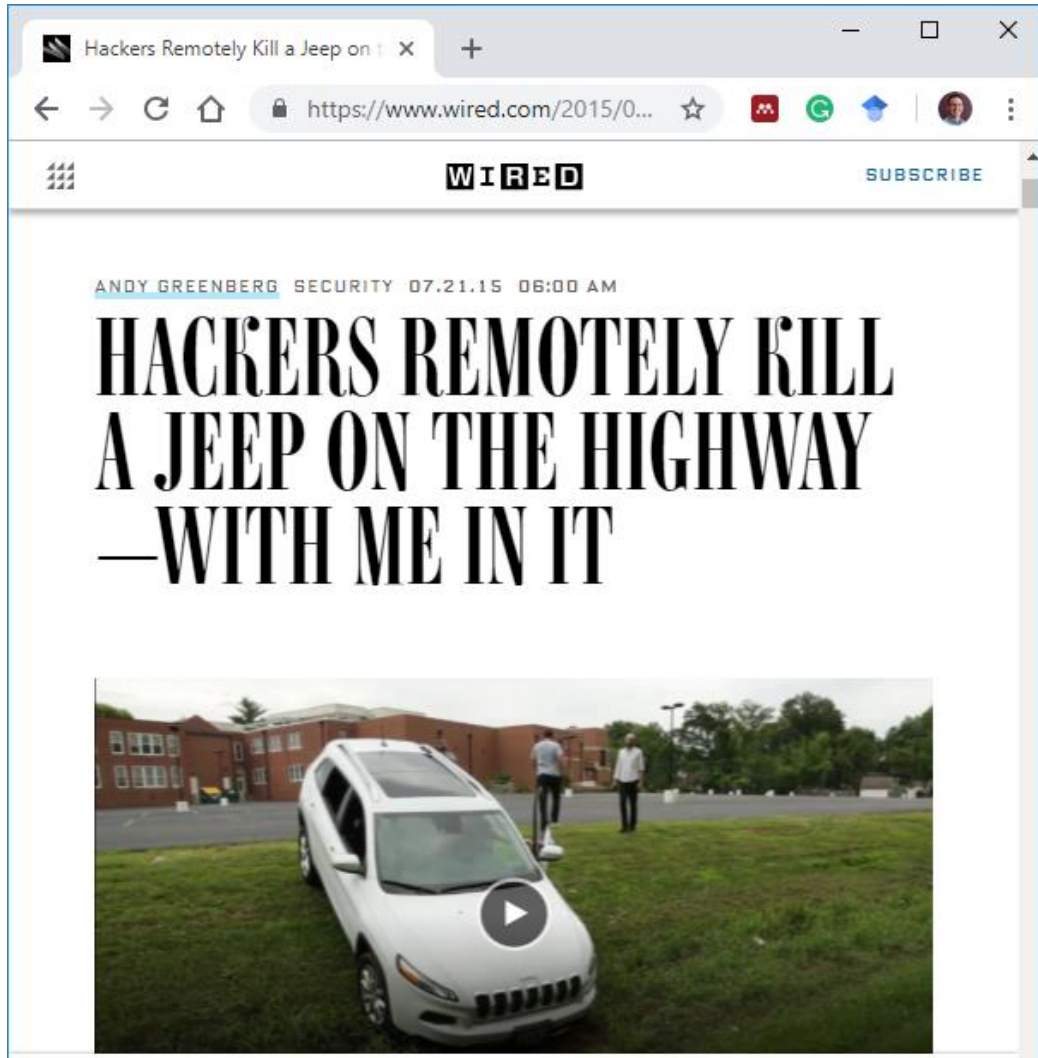
BeamNG simulator for braking source and sink data.



It's here at AESIN!

ABS model in C on CHERI "ECU"





## Memory manipulation is not the only issue

The seminal 2015 Jeep hack was down to poor security controls and a lack of authentication checks – no memory manipulation was required

**CHERI would be part of the solution**



Lead Partner



Demonstrator



Dissemination

Please give us your thoughts:  
[dan.fowler@warwick.ac.uk](mailto:dan.fowler@warwick.ac.uk)



Funding:

